



Docket No: 740756-1638

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT Application of:)
Hisashi OHTANI et al.)
Serial No. 08/807,737) ISSUE BRANCH
Filed: February 27, 1997)
For: METHOD FOR MANUFACTURING)
SEMICONDUCTOR DEVICE)

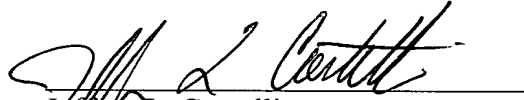
**SUBMISSION OF CORRECTED FORMAL DRAWINGS
IN RESPONSE TO NOTICE OF ALLOWABILITY**

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowability dated April 8, 2003, submitted herewith are four (4) sheets of corrected formal drawings, consisting of Figures 1A-4F, for filing in the subject application.

Respectfully submitted,



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